

Atty. Dkt. No. 088245-9240
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Applicant: PEPE, Angel Antonio

Title: METHOD OF MAKING STACKABLE
LAYERS CONTAINING
ENCAPSULATED INTEGRATED
CIRCUIT CHIPS WITH ONE OR MORE
OVERLAYING INTERCONNECT
LAYERS

Appl. No.: 09/938,686

Filing Date: October 30, 2001

Examiner: VU, Hung K.

Art Unit: 2811

Confirmation Number: 2551

CERTIFICATE OF FACSIMILE TRANSMISSION I hereby certify that this paper is being facsimile transmitted to the United States Patent and Trademark Office, Alexandria, Virginia on the date below. Billie M. Wells (Printed Name) Billie M. Wells (Signature) June 12, 2009 (Date of Deposit)
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**NOTIFICATION OF LOSS OF ENTITLEMENT TO
SMALL ENTITY STATUS**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

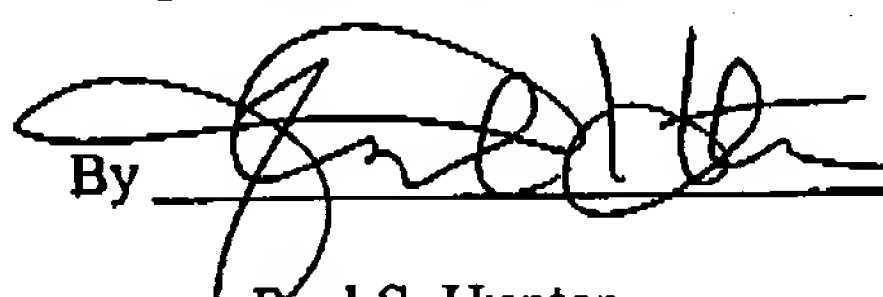
Pursuant to 37 C.F.R. §1.27(g)(2), Applicant hereby states that small entity status in the above-identified application is no longer appropriate. Fees paid presently and henceforth will be paid based on large entity status.

Respectfully submitted,

Date June 12, 2009

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